Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	1253	345/531,532,536,537.ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/26 13:57
L4	800	3 and (memory near2 control\$4)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/26 13:57
L5	569	4 and register	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/26 13:57
L6	312	5 and (memory near2 (array block bank))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/26 14:11
L7	103	6 and (access\$3 with request)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/26 14:11
L19	7888	((multiple plural\$3 (first and second)) near2 (memory near2 control\$4))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/26 14:50
L20	3705	19 and (memory near2 (array block bank))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/26 14:11
L21	1129	20 and (access\$3 with request)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/26 14:16
L22	1254	20 and (access\$3 with request\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/26 14:20
L23	8	22 and ((functional and faulty) near3 memory)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/26 14:29
L24	270	22 and (function\$2 adj3 (block array bank))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/26 14:30
L25	325	22 and (function\$2 near3 (block array bank)) .	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/26 14:47
L26	22	22 and ((function\$2 near3 (block array bank)) with (valid indicat\$3 flag))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/26 14:31
L27	84	25 and (register with field)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/26 14:51
L29	491	"345"/\$.ccls. and ((multiple plural\$3 (first and second)) near2 (memory near2 control\$4))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/26 14:50

L30	54	29 and (register with field)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/26 14:51
L31	1015	711/5.cds.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/26 14:55
L32	206	19 and 31	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2005/05/26 14:58
S1	0	345/521.cds.	US-PGPUB; USPAT	OR	ON	2002/12/09 11:33
S2	285	345/102.cds.	US-PGPUB; USPAT	OR	ON	2002/12/09 11:37
S 3	0	345/102.cds. and (embedded near memory)	US-PGPUB; USPAT	OR	ON	2002/12/09 11:34
S4	0	345/102.ccls. and (array near memory)	US-PGPUB; USPAT	OR	ON	2002/12/09 11:34
S5	1708	embedded near memory	US-PGPUB; USPAT	OR	ON	2002/12/09 11:37
S6	7	(embedded near memory) and (start near address) and (memory near size)	US-PGPUB; USPAT	OR	ON	2002/12/09 12:04
S 7	282	james near peterson	US-PGPUB; USPAT	OR	ON	2002/12/09 11:47
S8	901	711/173	US-PGPUB; USPAT	OR	ON	2002/12/09 11:47
S9	15	711/173 and (embed\$3 near memory)	US-PGPUB; USPAT	OR	ON	2002/12/09 11:48
S11	72	345/544	US-PGPUB; USPAT	OR	ON	2002/12/09 12:07
512	613	faulty near memory	US-PGPUB; USPAT	OR	ON	2002/12/09 12:07
S13	271	(faulty near memory) and (memory near array)	US-PGPUB; USPAT	OR	ON	2002/12/09 12:07
S14	44	((faulty near memory) and (memory near array)) and (memory near controller)	US-PGPUB; USPAT	OR	ON	2002/12/09 12:26
S16	414	345/521	US-PGPUB; USPAT	OR	ON	2002/12/09 12:33
S24	8	rao.in. and ((fault\$3 or error) near memory)	US-PGPUB; USPAT	OR	ON	2002/12/09 13:12
S45	31267	memory near controller	US-PGPUB; USPAT	OR	ON	2004/04/26 13:42
S46	65	(memory near controller) and (disabl\$3 with faulty)	US-PGPUB; USPAT	OR	ON	2004/04/26 13:46
S50	71	(memory near2 control\$4) with (pass\$3 adj3 request)	US-PGPUB; USPAT	OR	ON	2004/04/27 10:11
S70	1968	(memory adj2 control\$4) with (request near3 access\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/18 09:43
S71	1053	S70 and (memory near2 (array bank block))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/18 09:43

S72	1284	(memory adj2 control\$4) with ((receiv\$3 forward\$3) adj3 request)	US-PGPUB;	OR	ON	2004/11/18 09:44
			USPAT; EPO; JPO; IBM_TDB			
S73	30	((plurality multiple (first and second)) adj2 (memory adj2 control\$4)) with ((receiv\$3 forward\$3) adj3 request)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/18 15:16
S78	· 827	345/531,532,536.ccls.	USPAT	OR	ON	2004/11/18 11:25
S79	18	S71 and S78	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/18 11:25
S80	27	S72 and S78	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/18 11:28
S81	395	((plural\$3 multiple (first and second)) adj2 (memory adj2 control\$4)) and ((forward\$3 send\$3 propagat\$3) with (memory adj2 control\$4))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/18 11:31
S82	138	((plural\$3 multiple (first and second)) adj2 (memory adj2 control\$4)) and ((forward\$3 send\$3 propagat\$3) with request with (memory adj2 control\$4))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/18 14:57
S83	0	(((host system) and graphis) near processor) and (memory near controller)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/18 14:58
S84	618	(((host system) and graphics) near processor) and (memory near controller)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/18 15:13
S85	314	S84 and (system near bus)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/18 15:14
S86	160	S85 and (display adj device)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/18 15:14
S87	618	(((host system) and graphics) near processor) and (memory near controllers)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/18 15:13
S88	242	S87 and (display adj device)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/18 15:13
S89	314	S87 and (system near bus)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR .	ON	2004/11/18 15:15
S90	160	S89 and (display adj device)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/18 15:15
S91	0	S82 and S90	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/18 15:15

S92	20	S89 and ((plurality multiple (first and second)) adj2 (memory adj2	US-PGPUB;	OR	ON	2004/11/18 15:16
		control\$4))	USPAT;			
		· · · · · ·	EPO; JPO;			
			IBM_TDB	Ì		